

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	279	712/226.ccls.	USPAT	OR	OFF	2006/09/04 19:27
L2	150190	flash	USPAT	OR	OFF	2006/09/04 19:27
L3	22440	dma	USPAT	OR	OFF	2006/09/04 19:27
L4	27823	sram	USPAT	OR	OFF	2006/09/04 19:27
L5	4179	2 and 3	USPAT	OR	OFF	2006/09/04 19:27
L6	1230	4 and 5	USPAT	OR	OFF	2006/09/04 19:27
L7	395592	microprocessor or microcomputer or (integrated adj circuit)	USPAT	OR	OFF	2006/09/04 19:27
L8	1016	6 and 7	USPAT	OR	OFF	2006/09/04 19:27
L9	284	2 with 3	USPAT	OR	OFF	2006/09/04 19:27
L10	53	4 with 9	USPAT	OR	OFF	2006/09/04 19:28
L11	46	7 and 10	USPAT	OR	OFF	2006/09/04 19:28
L12	10104	charge adj pump	USPAT	OR	OFF	2006/09/04 19:28
L13	497	2 with 12	USPAT	OR	OFF	2006/09/04 19:28
L14	142	2 near3 12	USPAT	OR	OFF	2006/09/04 19:28
L15	8106	data adj port	USPAT	OR	OFF	2006/09/04 19:28
L16	380	(instruction or code) adj port	USPAT	OR	OFF	2006/09/04 19:28
L17	155	15 and 16	USPAT	OR	OFF	2006/09/04 19:28
L18	664	embedd\$5 near3 flash	USPAT	OR	OFF	2006/09/04 19:28
L19	860	8 and "19"	USPAT	OR	OFF	2006/09/04 19:29
L20	0	14 and 17	USPAT	OR	OFF	2006/09/04 19:29
L21	7	8 and 18	USPAT	OR	OFF	2006/09/04 19:29
L22	6	12 with 18	USPAT	OR	OFF	2006/09/04 19:30
L23	68980	(interface or path) near3 (exten\$5 or widen\$3)	USPAT	OR	OFF	2006/09/04 19:31
L24	6	2 with 22	USPAT	OR	OFF	2006/09/04 19:31
L25	28	2 near3 23	USPAT	OR	OFF	2006/09/04 19:31
L26	102	2 with 23	USPAT	OR	OFF	2006/09/04 19:31
L27	231	flash with slave	USPAT	OR	OFF	2006/09/04 19:31
L28	0	slave near2 interface with 15	USPAT	OR	OFF	2006/09/04 19:32
L29	92	flash near3 slave	USPAT	OR	OFF	2006/09/04 19:32
L30	387	coprocessor near3 interface	USPAT	OR	OFF	2006/09/04 19:32
L31	0	flash near3 30	USPAT	OR	OFF	2006/09/04 19:32
L32	4	flash with 30	USPAT	OR	OFF	2006/09/04 19:32
L33	4517	configuration adj register\$1	USPAT	OR	OFF	2006/09/04 19:32
L34	427	flash with fpga	USPAT	OR	OFF	2006/09/04 19:33

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L35	0	coprocessor with 34	USPAT	OR	OFF	2006/09/04 19:33
L36	15	coprocessor and 34	USPAT	OR	OFF	2006/09/04 19:33
L37	1	configuration adj register\$1 and 36	USPAT	OR	OFF	2006/09/04 19:33
L38	32312	variable adj (width or length)	USPAT	OR	OFF	2006/09/04 19:33
L39	8823	7 and 38	USPAT	OR	OFF	2006/09/04 19:34
L40	1266	39 and flash	USPAT	OR	OFF	2006/09/04 19:34
L41	135	6 and 40	USPAT	OR	OFF	2006/09/04 19:34
L42	1130	variable adj (width or length) near3 instruction\$1	USPAT	OR	OFF	2006/09/04 19:34
L43	135	40 and 41	USPAT	OR	OFF	2006/09/04 19:35
L44	22	41 and fpga	USPAT	OR	OFF	2006/09/04 19:35